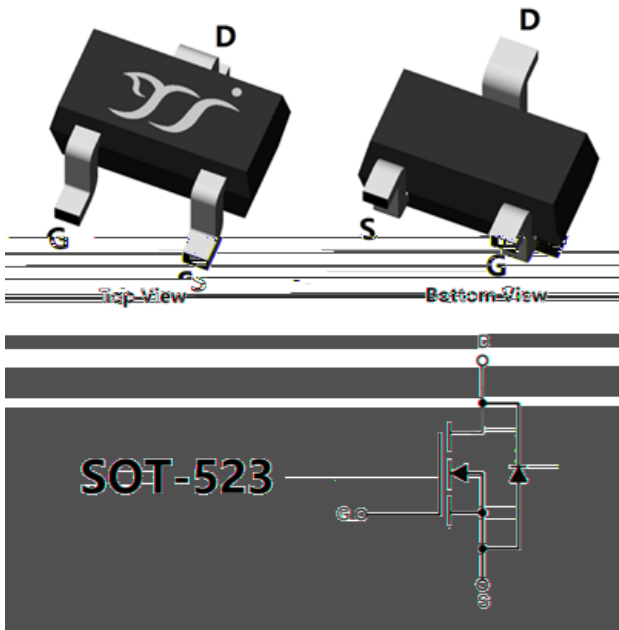




## N-Channel Enhancement Mode Field Effect Transistor



### Product Summary

|                                  |      |
|----------------------------------|------|
| $V_{DS}$                         | 50V  |
| $I_D$                            | 0.3A |
| $R_{DS(ON)}$ (at $V_{GS}=10V$ )  | 1    |
| $R_{DS(ON)}$ (at $V_{GS}=4.5V$ ) | 1.1  |

### General Description

- Trench Power MV MOSFET technology
- Voltage controlled small signal switch
- Low input Capacitance
- Fast Switching Speed
- Low Input / Output Leakage
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

### Applications

- Battery operated systems
- Solid-state relays
- Direct logic-level interface TTL/CMOS

### Absolute Maximum Ratings ( $T_A=25$ unless otherwise noted)

| Parameter                              | Symbol         | Limit             | Unit       |
|--|----------------|-------------------|------------|
| Drain-source Voltage                   | $V_{DS}$       | 50                | V          |
| Gate-source Voltage                    | $V_{GS}$       | $\pm 20$          | V          |
| Drain Current                          | $I_D$          | $T_A=25^\circ C$  | 0.3        |
|  |                | $T_A=100^\circ C$ | 0.19       |
| Pulsed Drain Current <sup>A</sup>      | $I_{DM}$       | 2                 | A          |
| Total Power Dissipation <sup>C</sup>   | $P_D$          | $T_A=25^\circ C$  | 0.32       |
|  |                | $T_A=100^\circ C$ | 0.13       |
| Junction and Storage Temperature Range | $T_J, T_{STG}$ | -55 +150          | $^\circ C$ |

### Thermal resistance

| Parameter   | Symbol   | Typ | Max | Units        |
|---|----------|-----|-----|--------------|
| Thermal Resistance Junction-to-Ambient <sup>D</sup> | $R_{JA}$ | 320 | 380 | $^\circ C/W$ |

### Ordering Information (Example)

| PREFERRED P/N | PACKING CODE | Marking | MINIMUM PACKAGE(pcs) | INNER BOX QUANTITY(pcs) | OUTER CARTON QUANTITY(pcs) | DELIVERY MODE |
|---------------|--------------|---------|----------------------|-------------------------|----------------------------|---------------|
| BSS138E       | F2           | SS.     | 3000                 | 30000                   | 120000                     | 7" reel       |



# BSS138E

RECOMMEND  
**BSS138BE**  
FOR NEW DESIGN

## Electrical Characteristics ( $T_J=25$ unless otherwise noted)

| Parameter                         | Symbol       | Conditions                               | Min | Typ  | Max       | Units   |
|-----------------------------------|--------------|--|-----|------|-----------|---------|
| <b>Static Parameter</b>           |              |  |     |      |           |         |
| Drain-Source Breakdown Voltage    | $BV_{DSS}$   | $V_{GS}=0V, I_D=250\mu A$                | 50  | -    | -         | V       |
| Zero Gate Voltage Drain Current   | $I_{DSS}$    | $V_{DS}=50V, V_{GS}=0V$                  | -   | -    | 1         | $\mu A$ |
|                                   |              | $V_{DS}=50V, V_{GS}=0V, T_J=150^\circ C$ | -   | -    | 100       |         |
| Gate-Body Leakage Current         | $I_{GSS}$    | $V_{GS}=\pm 20V, V_{DS}=0V$              | -   | -    | $\pm 100$ | nA      |
| Gate Threshold Voltage            | $V_{GS(th)}$ | $V_{DS}=V_{GS}, I_D=250\mu A$            | 0.8 | 1.2  | 1.6       | V       |
| Static Drain-Source On-Resistance | $R_{DS(ON)}$ | $V_{GS}=10V, I_D=0.3A$                   | -   | 0.7  | 1         |         |
|                                   |              | $V_{GS}=4.5V, I_D=0.2A$                  | -   | 0.78 | 1.1       |         |
| Diode Forward Voltage             | $V_{SD}$     | $I_S=0.3A, V_{GS}=0V$                    | -   | 0.85 | 1.2       |         |



## Typical Electrical and Thermal Characteristics Diagrams

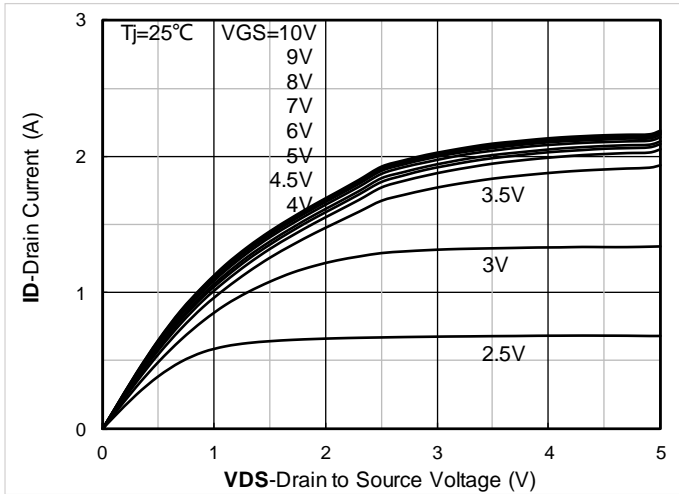


Figure 1. Output Characteristics

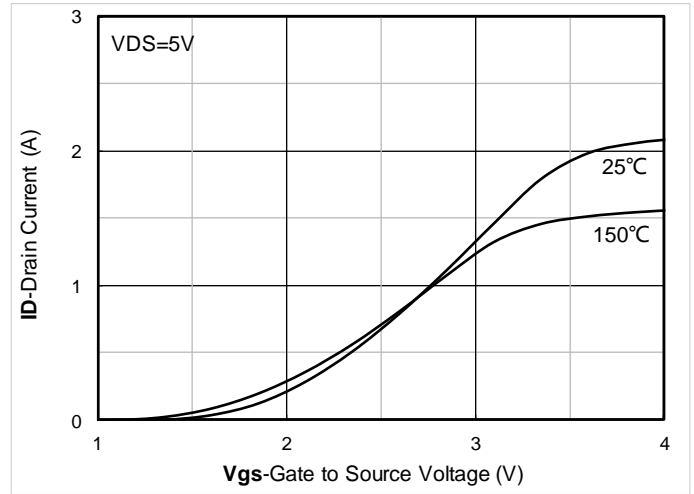


Figure 2. Transfer Characteristics

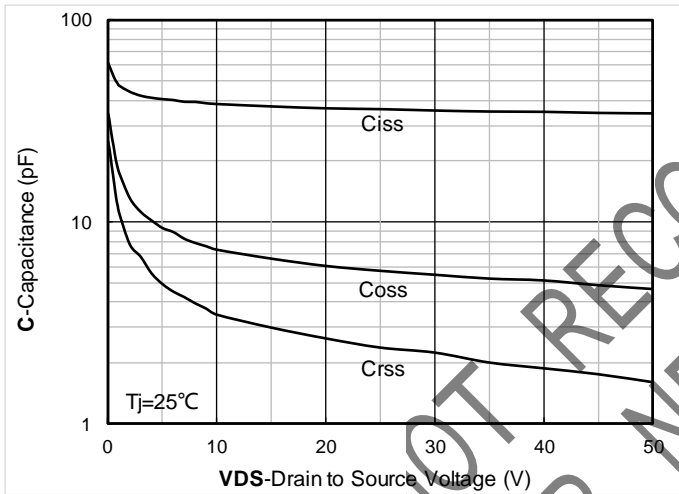


Figure 3. Capacitance Characteristics

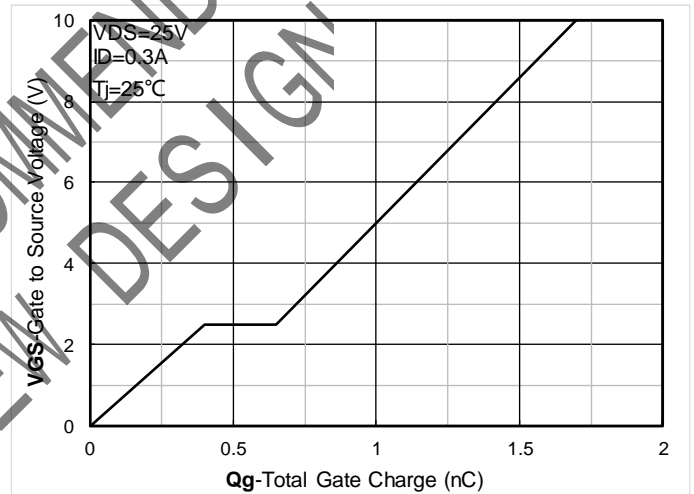


Figure 4. Gate Charge

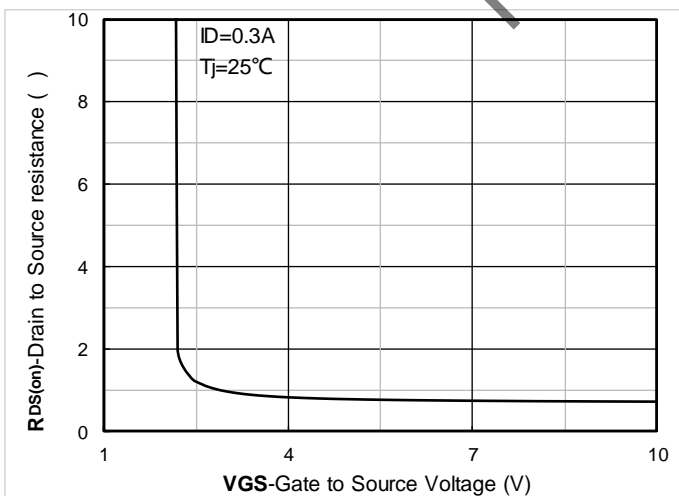


Figure 5. On-Resistance vs Gate to Source Voltage

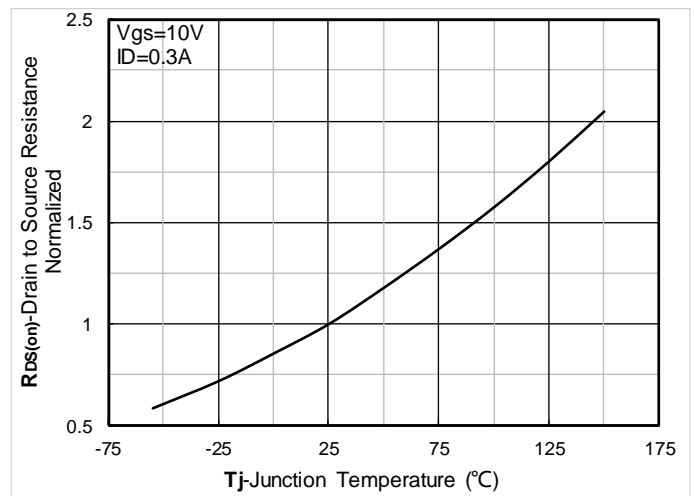


Figure 6. Normalized On-Resistance

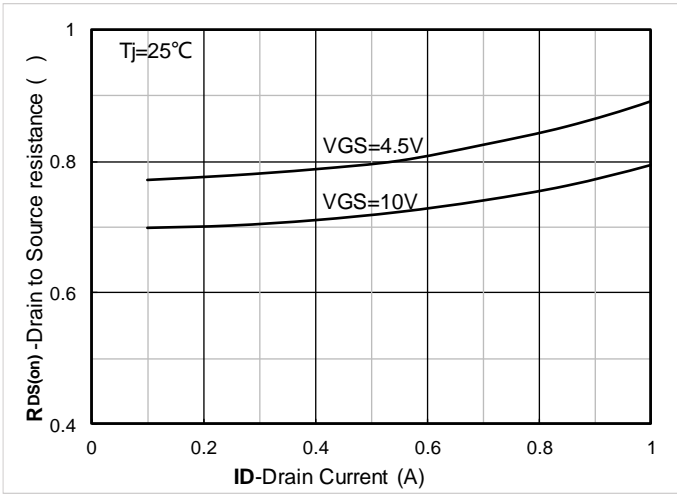


Figure 7.  $R_{DS(on)}$  VS Drain Current

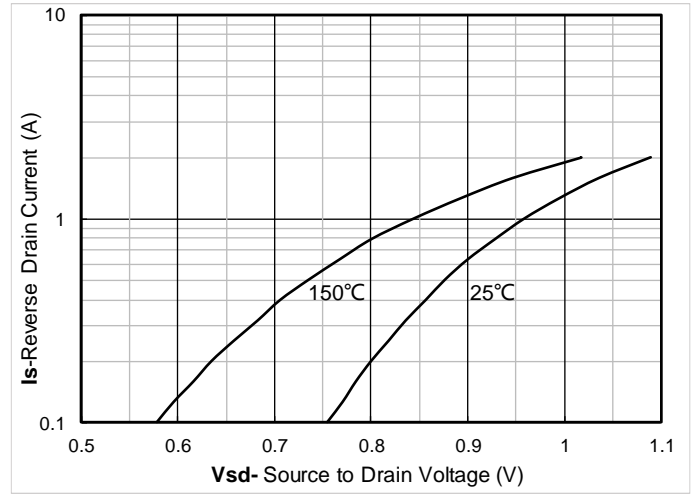


Figure 8. Forward characteristics of reverse diode

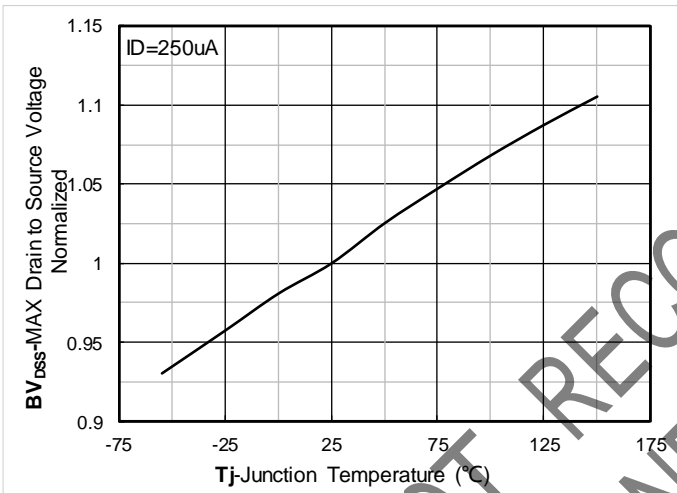


Figure 9. Normalized breakdown voltage

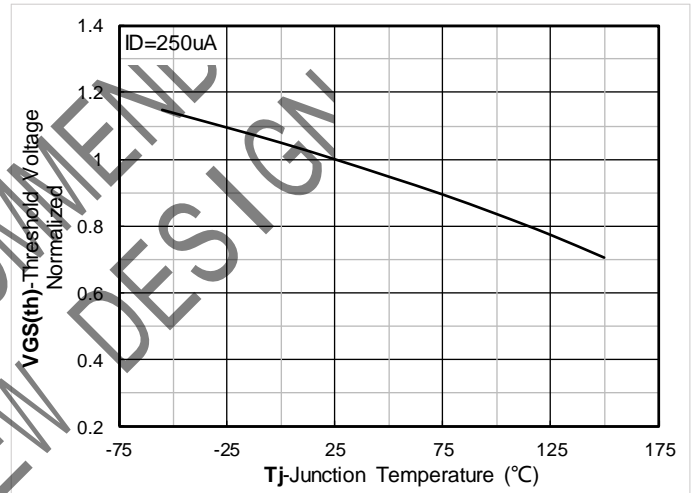


Figure 10. Normalized Threshold voltage

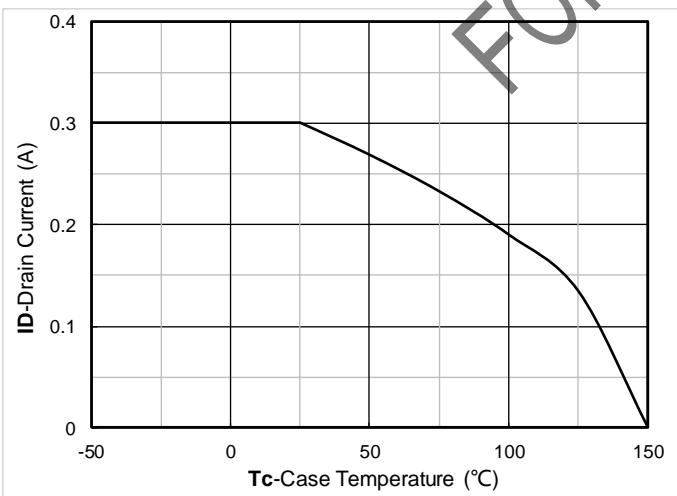


Figure 11. Current dissipation

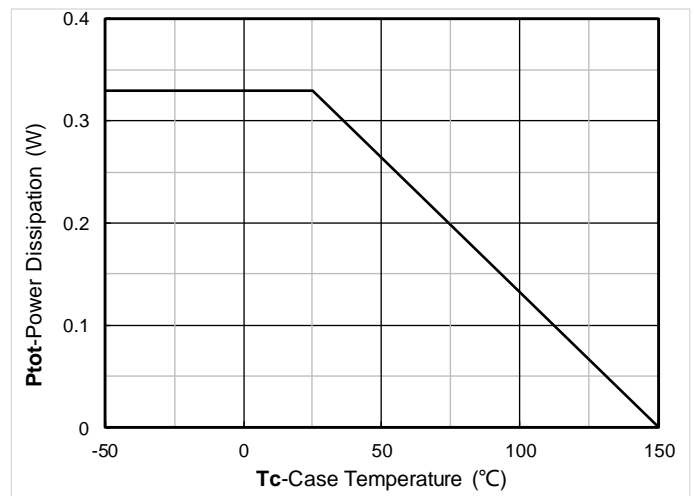


Figure 12. Power dissipation

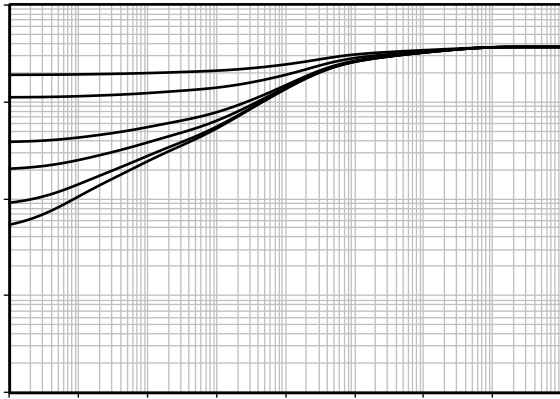


Figure 13. Maximum Transient Thermal Impedance

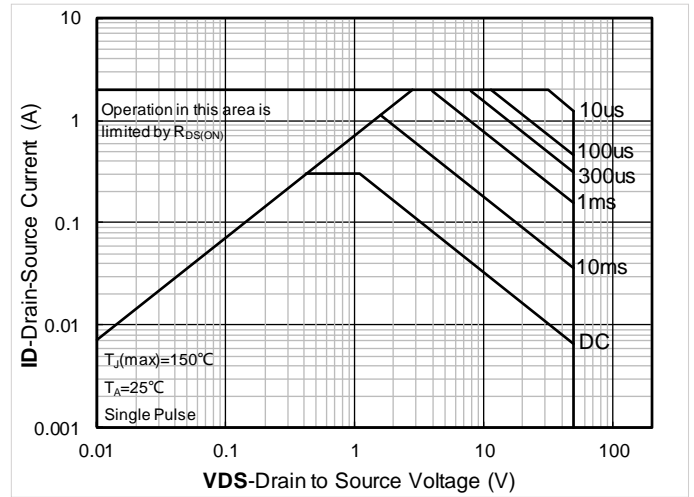


Figure 14. Safe Operation Area

## Test Circuits & Waveforms

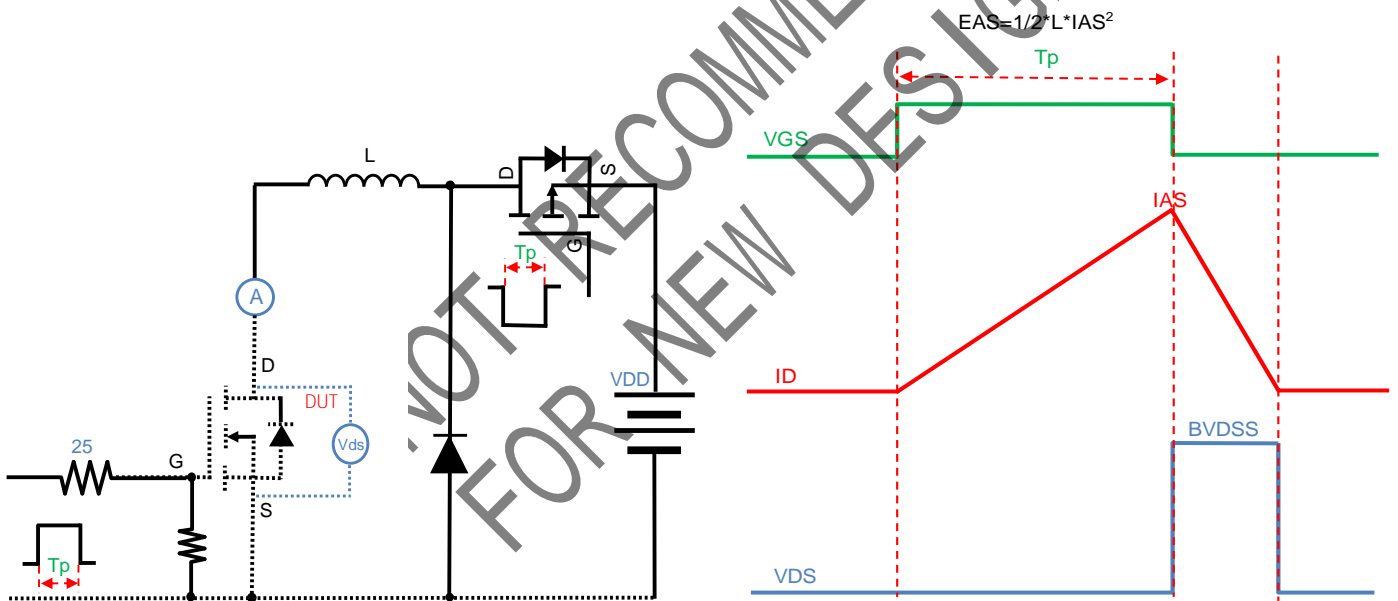


Figure A. Unclamped Inductive Switching (UIS) Test Circuit & Waveform

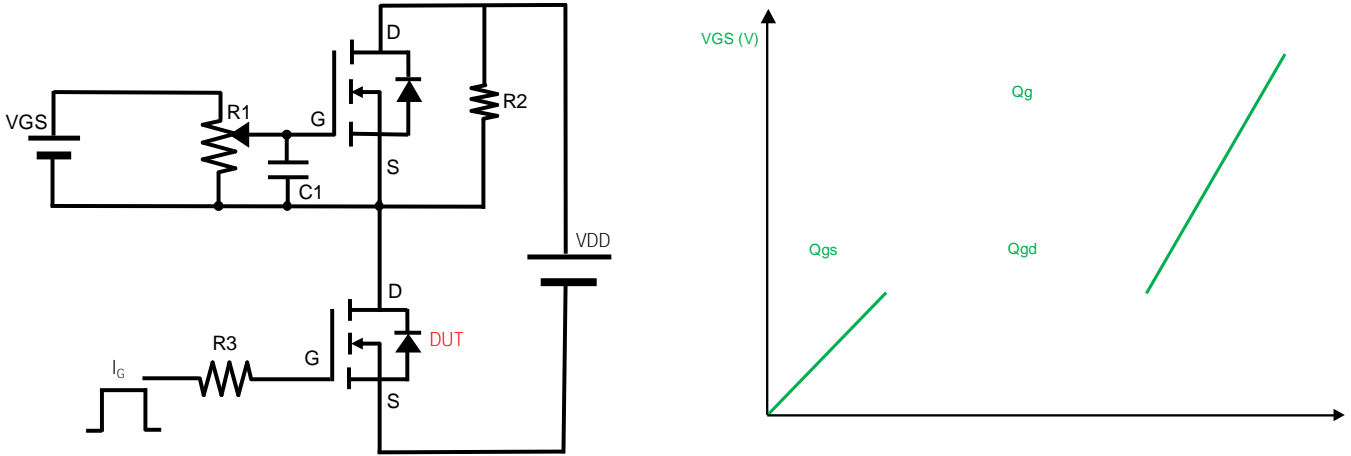


Figure B. Gate Charge Test Circuit & Waveform

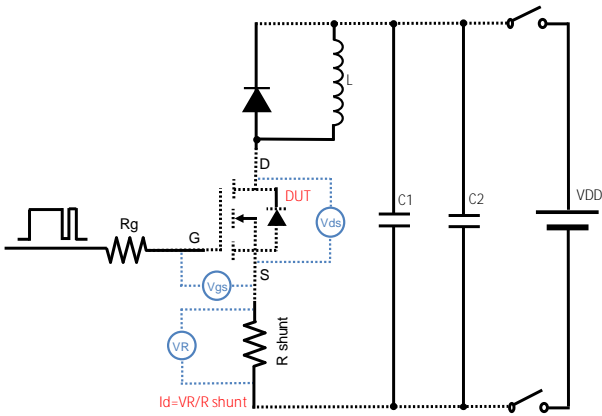
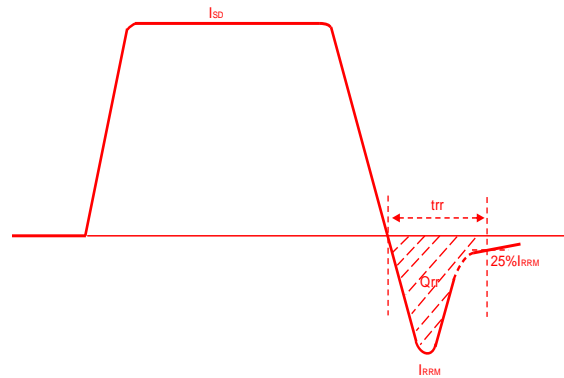
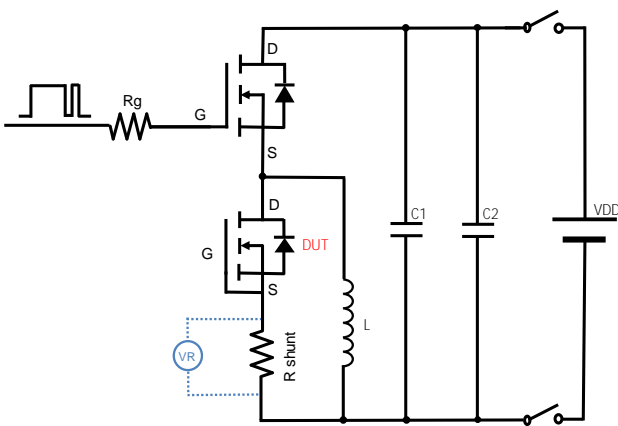
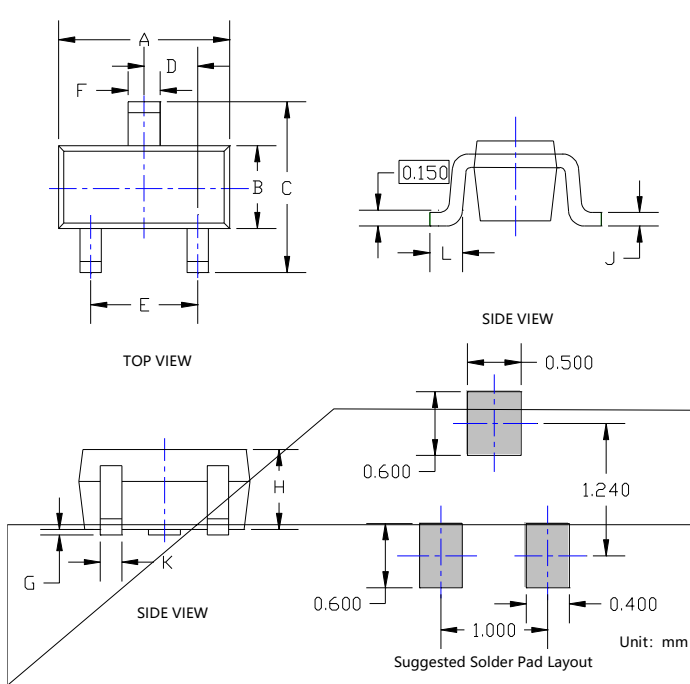


Figure C. Resistive Switching Test Circuit & Waveform





SOT-523 Package information



| SYMBOL | MIN      |
|--------|----------|
|        | 0.0      |
| B      |          |
| D      | 0.020TYP |
| E      |          |
| F      |          |
| H      |          |
| J      |          |
| K      |          |

NOTE:  
 1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.  
 2. TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.  
 3. THE PAD LAYOUT IS FOR REFERENCE PURPOSES ONLY.

NOT RECOMMENDED  
FOR NEW DESIGN



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