



BSS84KEJ

P-Channel Enhancement Mode Field Effect Transistor

Product Summary

V_{DS}	-60 V
I_D	-0.26 A
$R_{DS(ON)}$ (at $V_{GS}=-10V$)	<3
$R_{DS(ON)}$ (at $V_{GS}=-4.5V$)	<3.5
Gate-Source ESD Rating Up to 2KV (HBM)	

General Desc





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Typical Electrical and Thermal Characteristics Diagrams

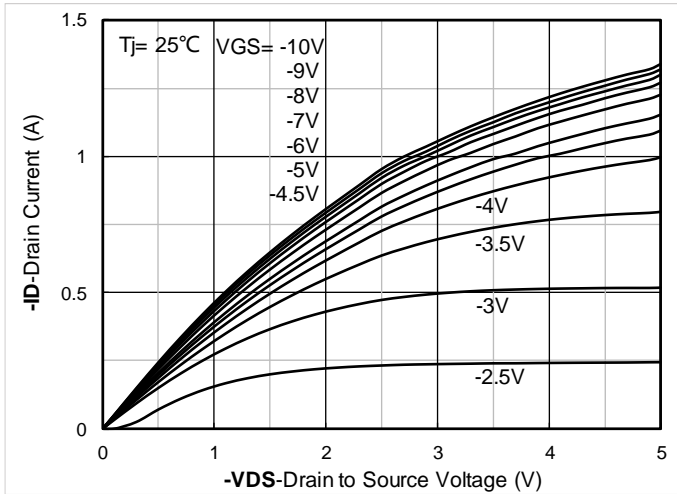


Figure 1. Output Characteristics

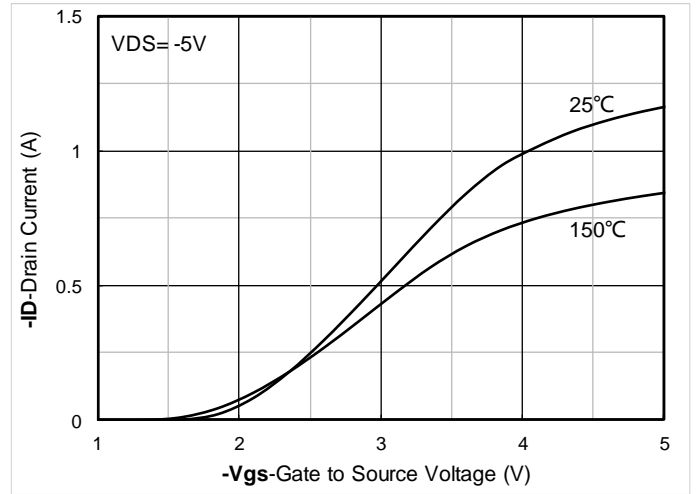


Figure 2. Transfer Characteristics

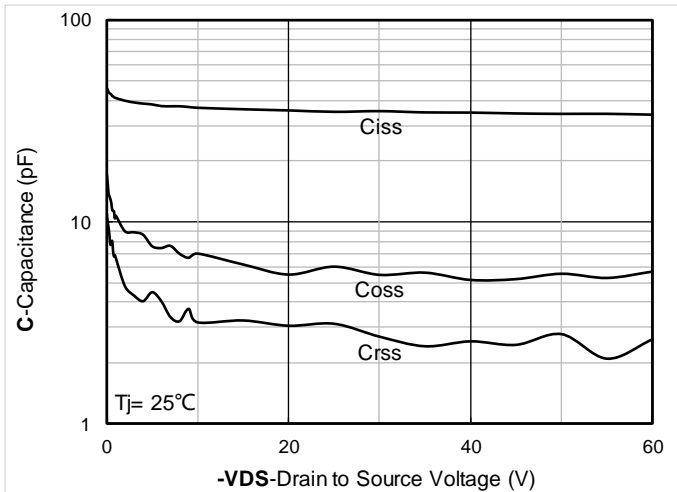


Figure 3. Capacitance Characteristics

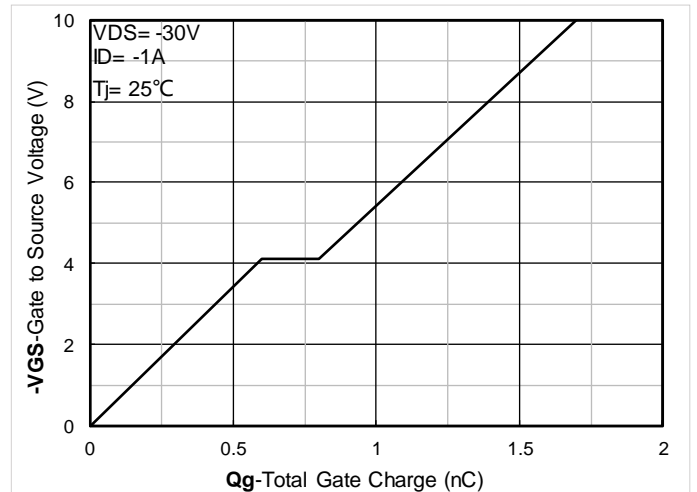


Figure 4. Gate Charge

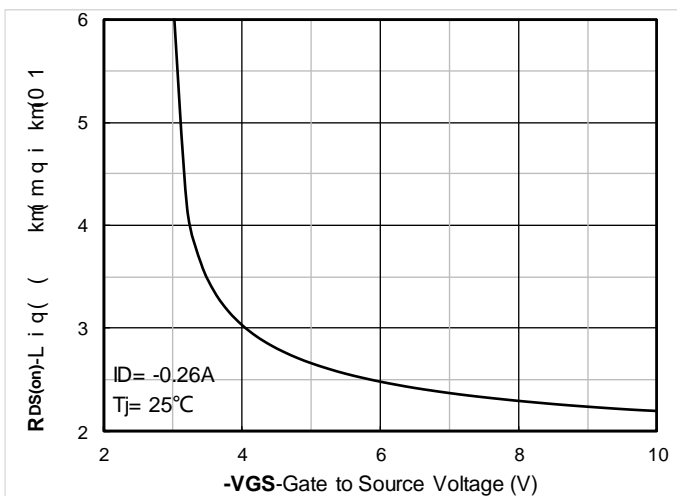


Figure 5. On-Resistance vs Gate to Source Voltage

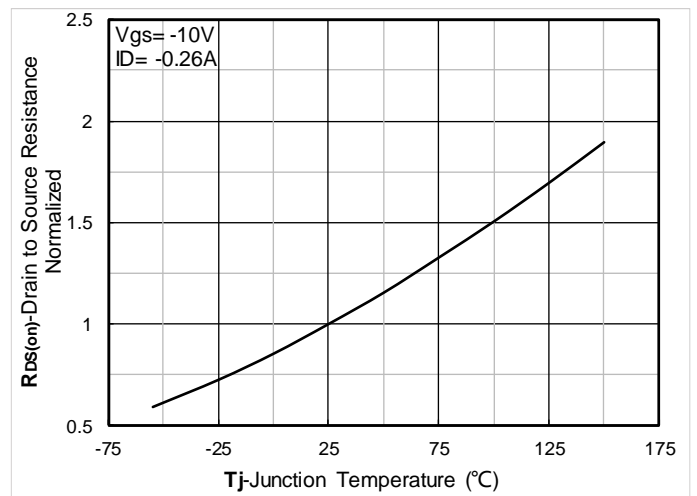


Figure 6. Normalized On-Resistance



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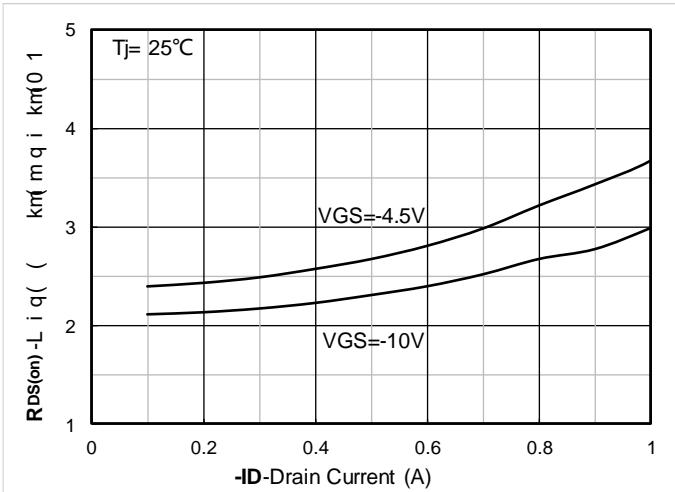


Figure 7. RDS(on) VS Drain Current

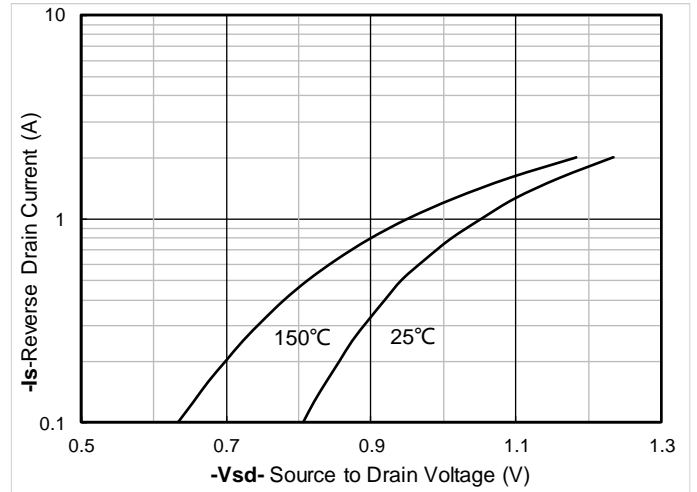


Figure 8. Forward characteristics of reverse diode

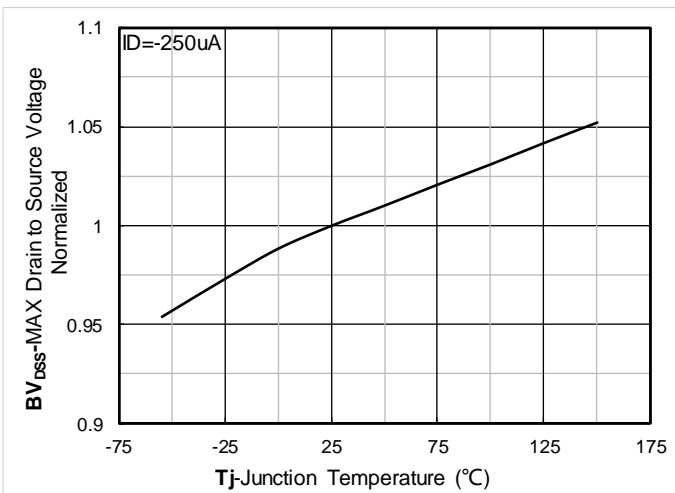


Figure 9. Normalized breakdown voltage

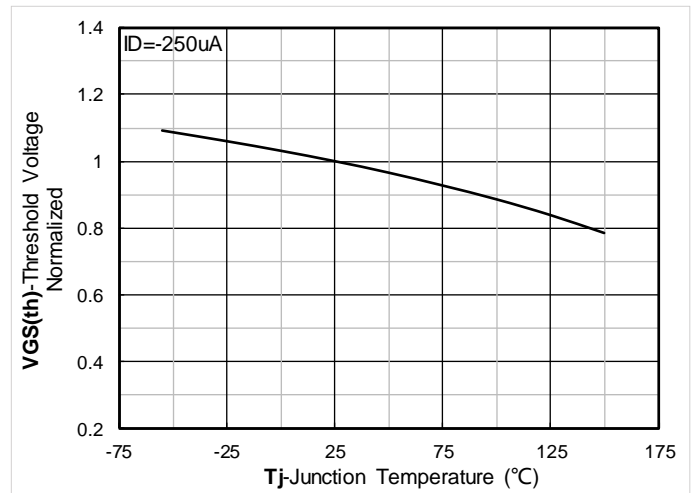


Figure 10. Normalized Threshold voltage

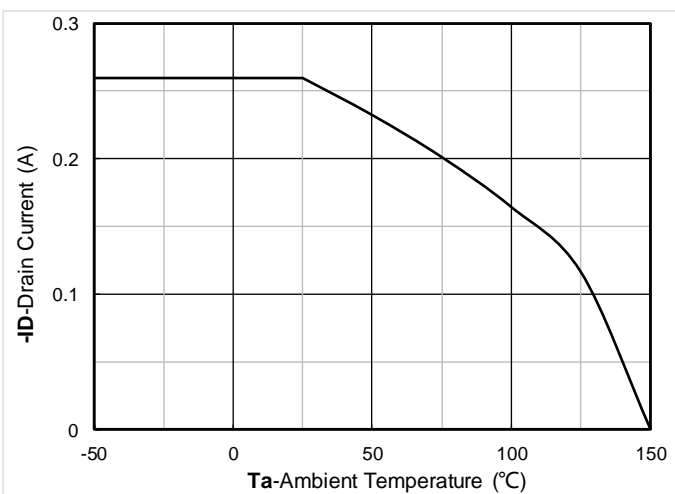


Figure 11. Current dissipation

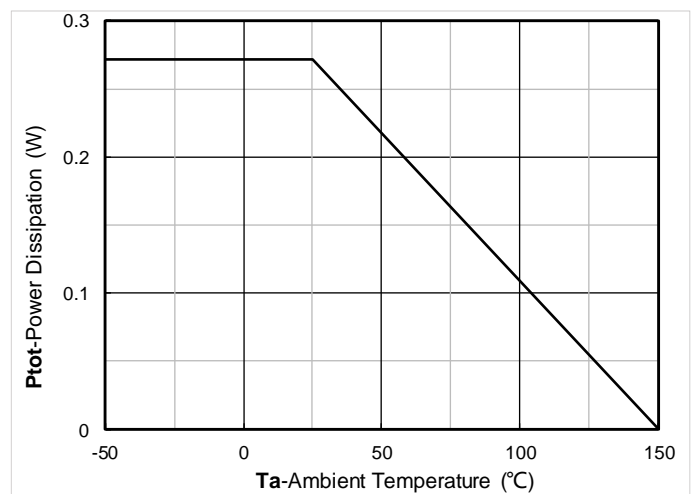


Figure 12. Power dissipation



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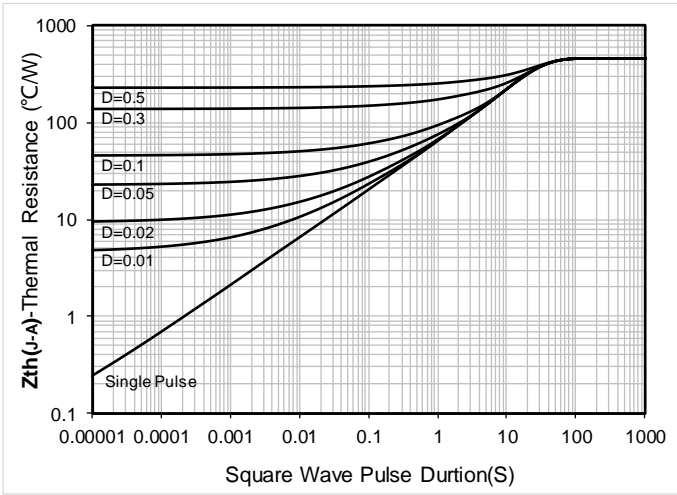


Figure 13. Maximum Transient Thermal Impedance

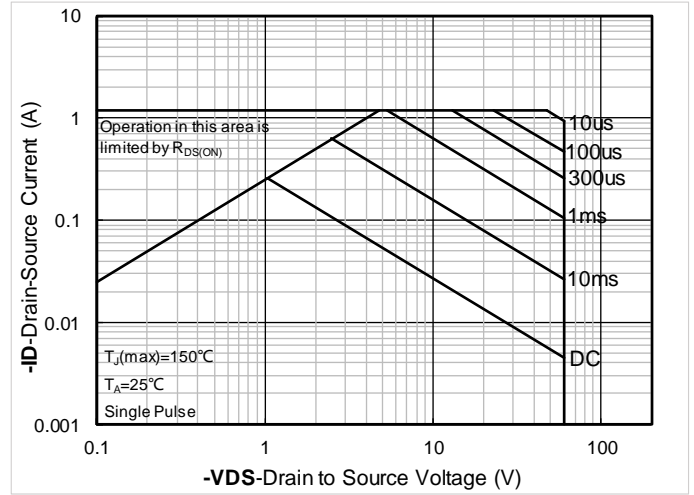
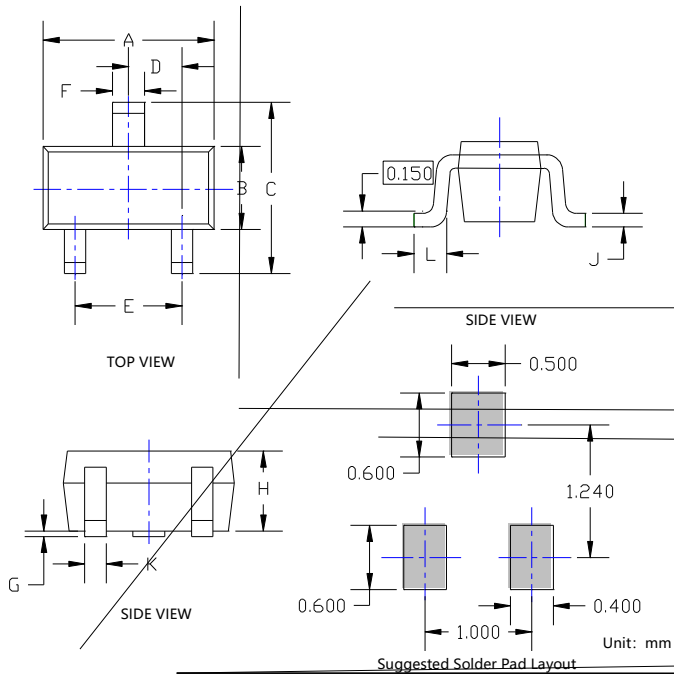


Figure 14. Safe Operation Area



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SOT-523 Package information



SYMBOL	DIMENSIONS		
	INCHES		Millimeter
	MIN.		MIN. MAX.
A	0.059		1.700
B	0.030		0.850
C	0.057		1.750
D	0.020TYP		
E	0.035		1.100
F	0.010		0.450
G			
H	0.024	(0.800
J	0.004		0.200
K	0.006		0.350

NOTE:
1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
2. TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.
3. THE PAD LAYOUT IS FOR REFERENCE PURPOSES ONLY.©



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