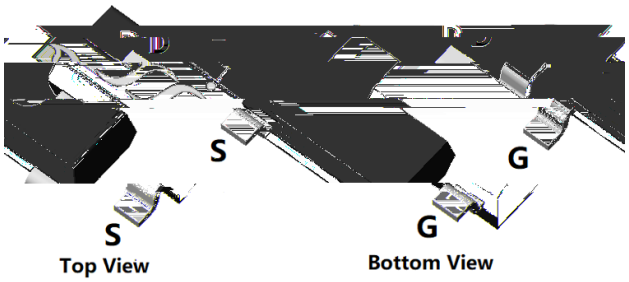
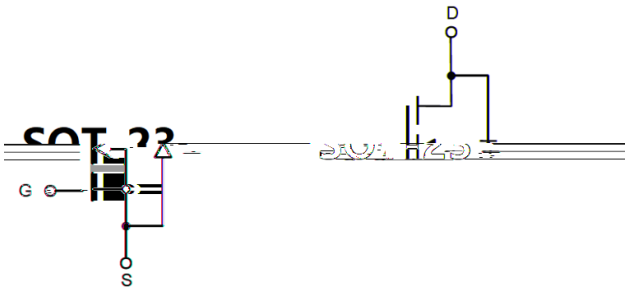
RECOMMEND
YJL2302A
FOR NEW DESIGN

N-Channel Enhancement Mode Field Effect Transistor





Product Summary

- V_{DS} 20V
- I_D 4.5A
- $R_{DS(ON)}$(at V_{GS}=4.5V) 27mΩ
- $R_{DS(ON)}$(at V_{GS}=2.5V) 35mΩ

General Description

- Trench Power MV MOSFET technology
- High Speed switching
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- PWM application
- Load switch

■ Absolute Maximum Ratings (T_A=25 unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-source Voltage		V_{DS}	20	V
Gate-source Voltage		V_{GS}	±10	V
Drain Current	T_A=25A	I_D	4.5	A
	T_A=100A		2.8	
Pulsed Drain Current [A]		I_{DM}	30	A
Total Power Dissipation [C]	T_A=25A	P_D	1	W
	T_A=100A		0.4	
Junction and Storage Temperature Range		T_J ,T_{STG}	-55 +150	A

■Thermal resistance

Parameter		Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient [D]	Steady-State	$R_{\theta JA}$	100	125	A /W

■ Ordering Information (Example)

PREFERED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJL2302C	F2	2302C.	3000	30000	120000	7“ reel

■ Electrical Characteristics (T_J=25 unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}= 0V, I_D=250μA	20	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=20V, V_{GS}=0V	-	-	1	μA
		V_{DS}=20V, V_{GS}=0V, Tj=150A	-	-	100	
Gate-Body Leakage Current	I_{GSS}	V_{GS}= ±10V, V_{DS}=0V	-	-	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}= V_{GS}, I_D=250μA	0.6	0.8	1.2	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	V_{GS}=4.5V, I_D=4.5A	-	21	27	mΩ
		V_{GS}=2.5V, I_D=3.0A	-	27	35	
Diode Forward Voltage	V_{SD}	I_S=4.5A, V_{GS}=0V	-	0.9	1.2	V
Gate resistance	R_G	f=1MHz, Open drain	-	3.3	-	Ω
Maximum Body-Diode Continuous Current	I_S		-	-	4.5	A
Dynamic Parameters						
Input Capacitance	C_{iss}		-	370	-	
Output Capacitance	C_{oss}	V_{DS}=10V, V_{GS}=0V, f=1MHz	-	80	-	pF
Reverse Transfer Capacitance	C_{rss}		-	70	-	
Switching Parameters						
Total Gate Charge	Q_g		-	11	-	
Gate-Source Charge	Q_{gs}	V_{GS}=10V, V_{DS}=10V, I_D=4.5A	-	1	-	nC
Gate-Drain Charge	Q_{gd}		-	2	-	
Reverse Recovery Charge	Q_{rr}	I_F=4.5A, di/dt=165A/us	-	4	-	nC
Reverse Recovery Time	t_{rr}		-	10	-	ns
Turn-on Delay Time	$t_{D(on)}$		-	3.5	-	
Turn-on Rise Time	t_r	V_{GS}=10V, V_{DD}=10V, I_D=4.5A R_{GEN}=2.2Ω	-	26	-	ns
Turn-off Delay Time	$t_{D(off)}$		-	19	-	
Turn-off fall Time	t_f		-	2.5	-	

A. Repetitive rating; pulse width limited by max. junction temperature.

B. P_d is based on max. junction temperature, using junction-case thermal resistance.

C. The value of $R_{\theta JA}$ [illegible]

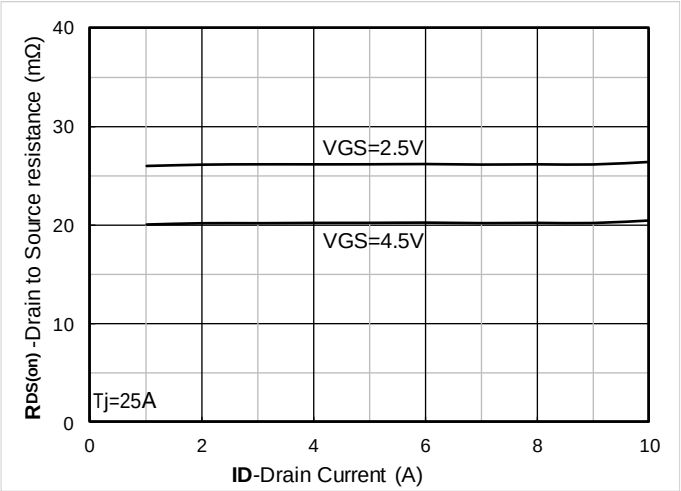


Figure 7. RDS(on) VS Drain Current

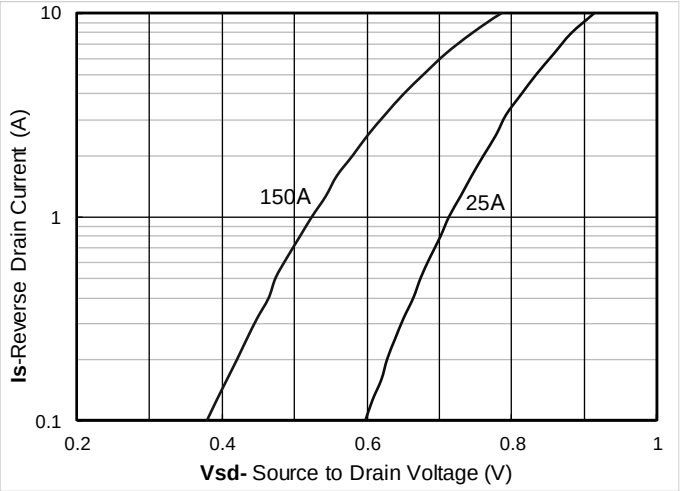


Figure 8. Forward characteristics of reverse diode

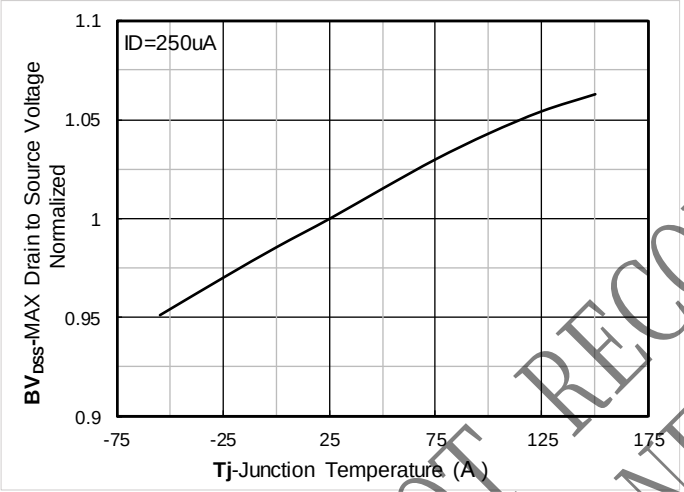


Figure 9. Normalized breakdown voltage

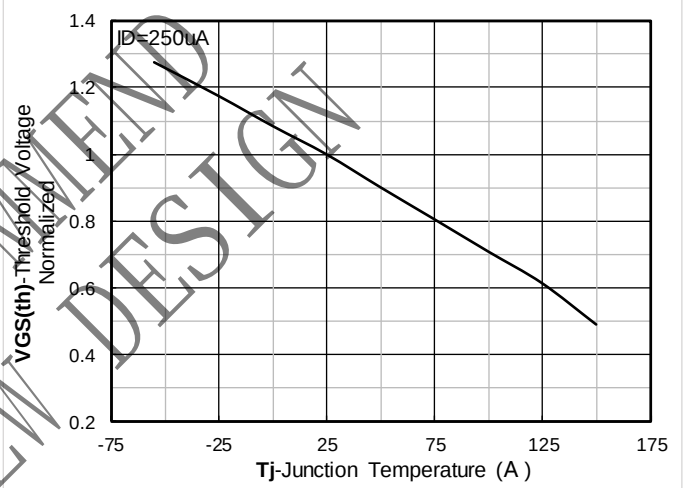


Figure 10. Normalized Threshold voltage

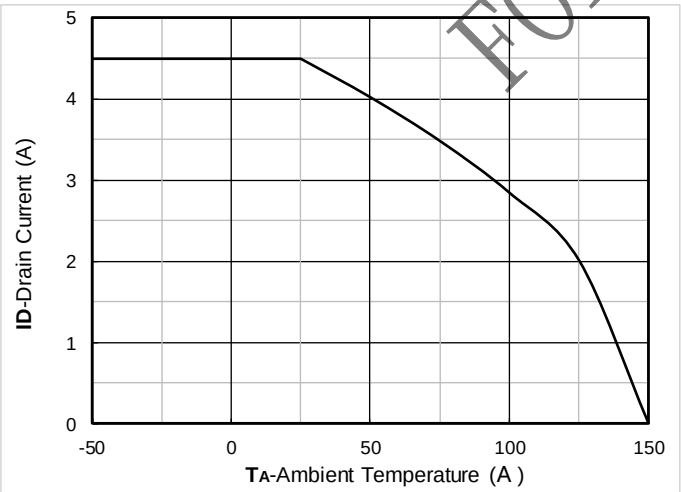


Figure 11. Current dissipation

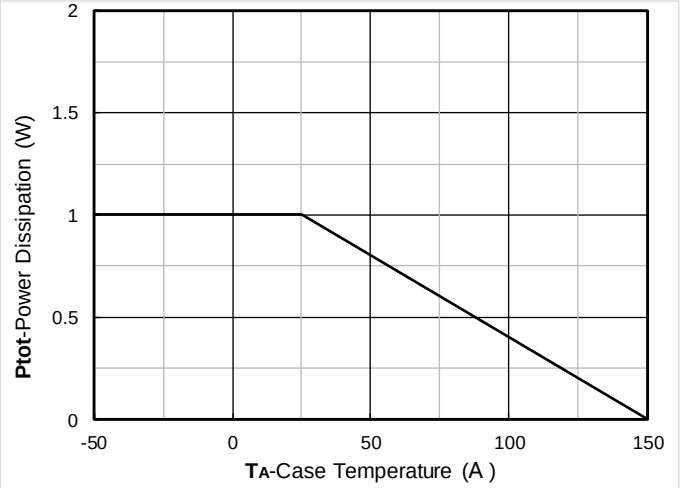


Figure 12. Power dissipation

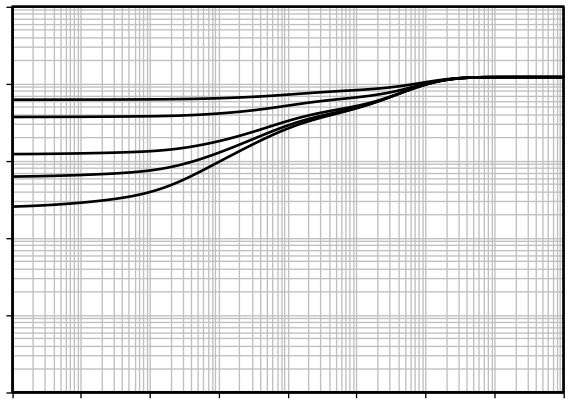


Figure 13. Maximum Transient Thermal Impedance

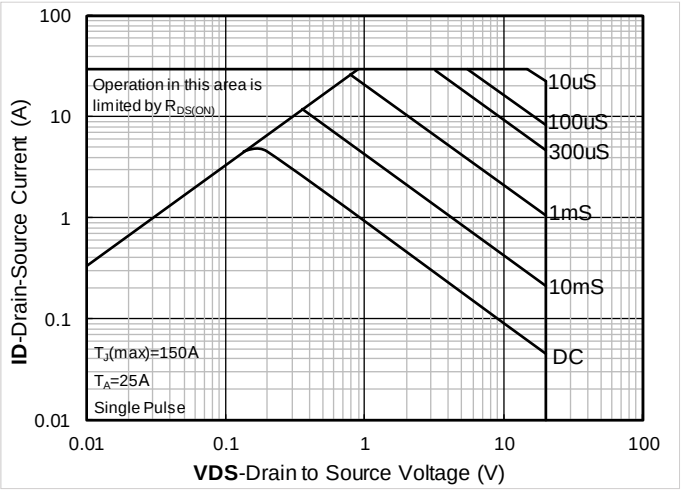


Figure 14. Safe Operation Area

■ Test Circuits & Waveforms

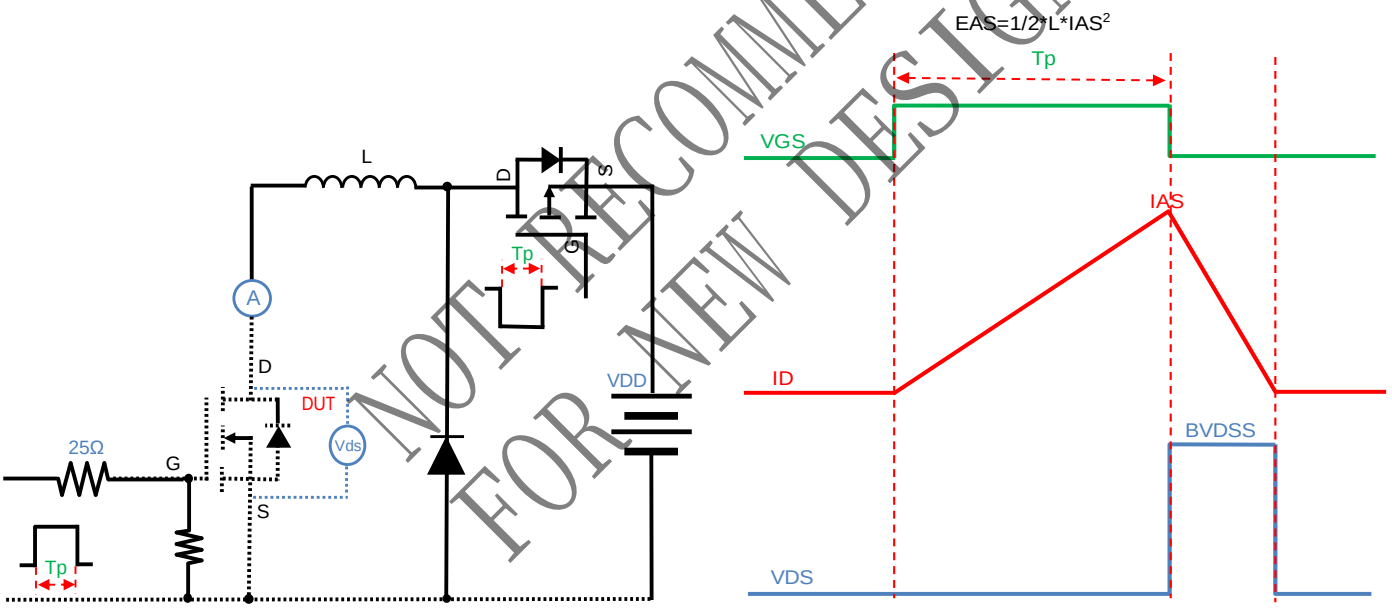


Figure A. Unclamped Inductive Switching (UIS) Test Circuit & Waveform

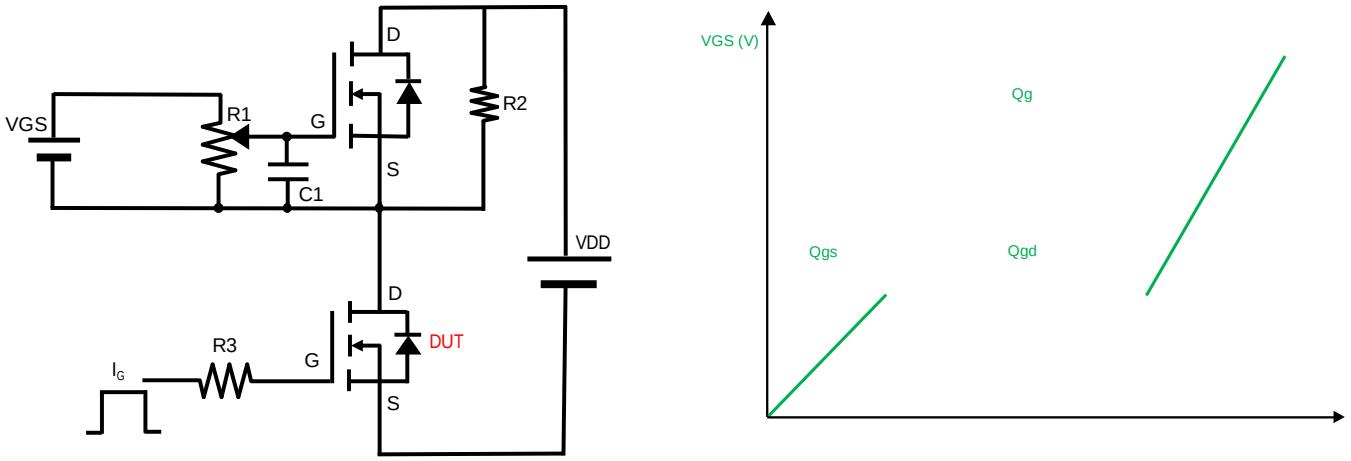


Figure B. Gate Charge Test Circuit & Waveform

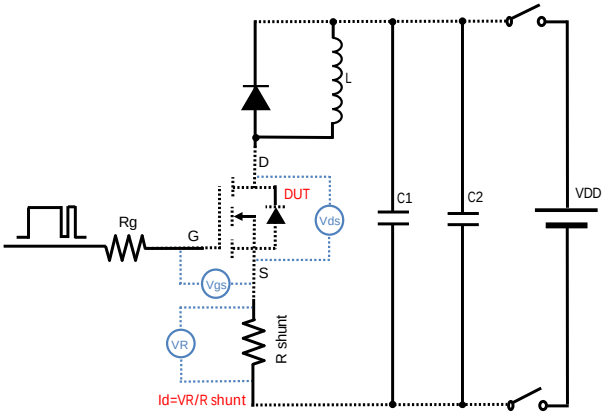


Figure C. Resistive Switching Test Circuit & Waveform

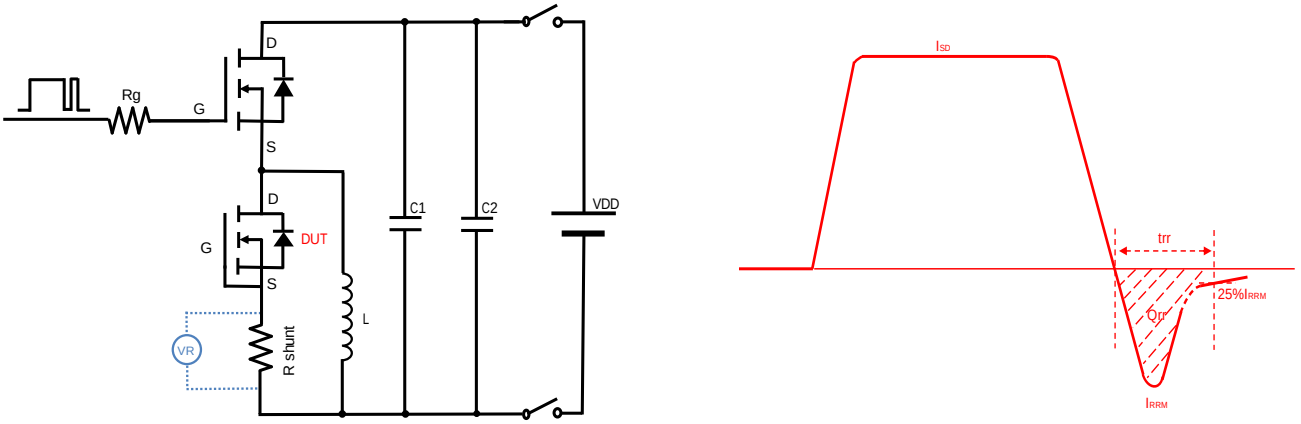


Figure D.

■ SOT-23 Package information

UNIT mm

Disclaimer

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