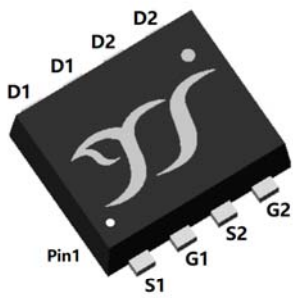


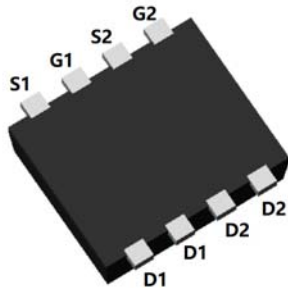


D-

RoHS
COMPLIANT

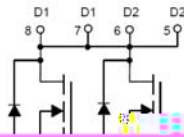


Top View



Bottom View

PDFN3030-8L



V_{DS}	20V
I_D	7A
$R_{DS(ON)}$ (at $V_{GS}=4.5V$)	17m
$R_{DS(ON)}$ (at $V_{GS}=2.5V$)	22m
$R_{DS(ON)}$ (at $V_{GS}=1.8V$)	39m
ESD Protected Up to 2KV (HBM)	

A

Trench Power LV MOSFET technology
High Speed switching
Moisture Sensitivity Level 1
Epoxy Meets UL 94 V-0 Flammability Rating
Halogen Free

PWM application
Load switch

($T_A=25$ unless otherwise noted)

Drain-source Voltage		V_{DS}	20	V
Gate-source Voltage		V_{GS}	± 10	V
Drain Current	$T_A=25$	I_D	7	A
	$T_A=100$		4	
Pulsed Drain Current ^A		I_{DM}	56	A
Total Power Dissipation ^B	$T_A=25$	P_D	1.25	W
	$T_A=100$		0.5	
Junction and Storage Temperature Range		T_J, T_{STG}	-55 +150	

Thermal Resista	θ_{JA}	θ_{JC}	θ_{CS}	r_{10D}
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C

(Example)

CA

CA

CC

C

C

YJUD3416A	F1	QD3416A	3000	30000	120000	7" reel
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D-

(T_J=25 unless otherwise noted)

Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D =250μA	20	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V, V _{GS} =0V	-	-	1	μA
		V _{DS} =20V, V _{GS} =0V, T _J =150	-	-	100	
Gate-Body Leakage Current	I _{GSS}	V _{GS} = ±10V, V _{DS} =0V	-	-	±10	μA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D =250μA	0.45	0.7	1	V
Static Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =7A	-	13	17	m
		V _{GS} =2.5V, I _D =4A	-	16	22	
		V _{GS} =1.8V, I _D =1.5A	-	25	39	
Diode Forward Voltage	V _{SD}	I _S =7A, V _{GS} =0V	-	-	1.2	V
Gate resistance	R _G	f=1MHz	-	2.5	-	
Maximum Body-Diode Continuous Current	I _S		-	-	7	A
Input Capacitance	C _{iss}	V _{DS} =10V, V _{GS} =0V, f=1MHz	-	615	-	pF
Output Capacitance	C _{oss}		-	125	-	
Reverse Transfer Capacitance	C _{rss}		-	105	-	
Total Gate Charge	Q _g	V _{GS} =4.5V, V _{DS} =10V, I _D =7A	-	8.3	-	nC
Gate-Source Charge	Q _{gs}		-	1.9	-	
Gate-Drain Charge	Q _{gd}		-	2.2	-	
Reverse Recovery Charge	Q _{rr}	I _F =7A, di/dt=100A/us	-	5.3	-	nC
Reverse Recovery Time	t _{rr}		-	25.5	-	ns
Turn-on Delay Time	t _{D(on)}	V _{GS} =4.5V, V _{DD} =10V, I _D =7A R _{GEN} =3	-	4	-	ns
Turn-on Rise Time	t _r		-	32	-	
Turn-off Delay Time	t _{D(off)}		-	19	-	
Turn-off fall Time	t _f		-	10	-	

A. Repetitive rating; pulse width limited by max. junction temperature.

B. P_d is based on max. junction temperature, using junction-ambient thermal resistance.

C. The value of R_{JA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C, The maximum allowed junction temperature of 150 . The value in any given application depends on the user's specific board design.

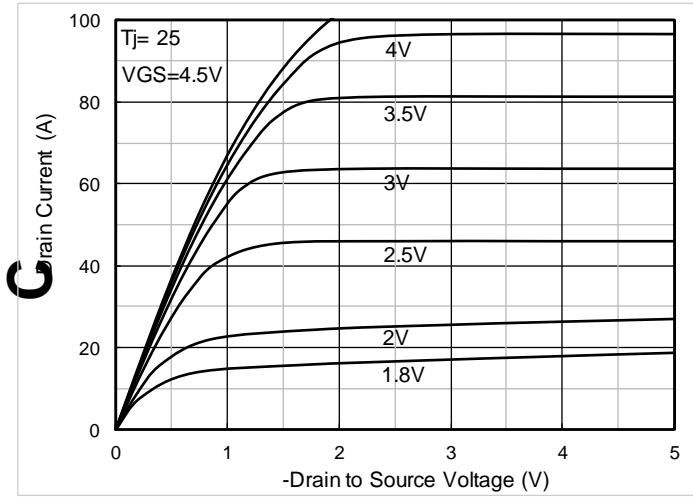


Figure 1. Output Characteristics

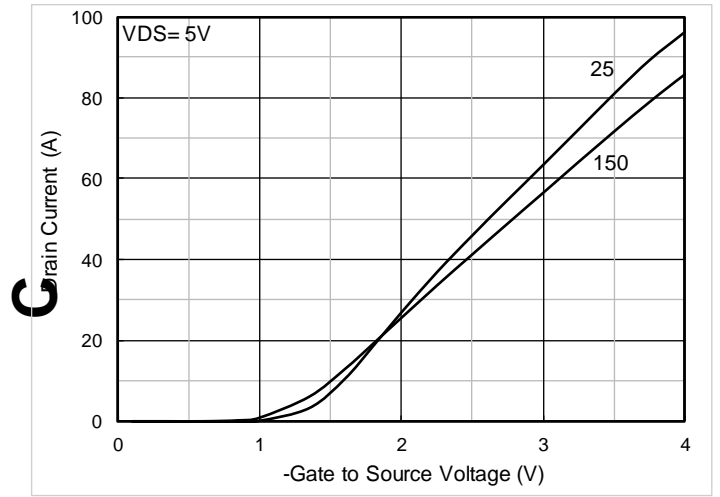
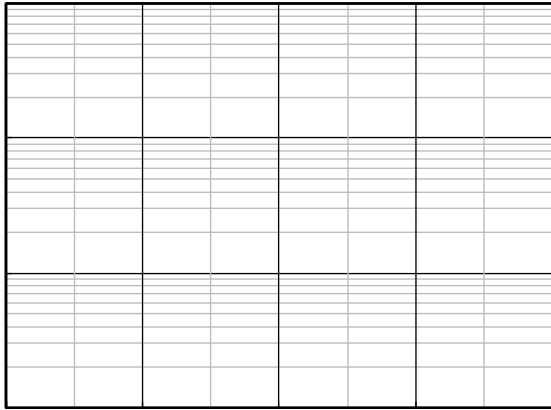
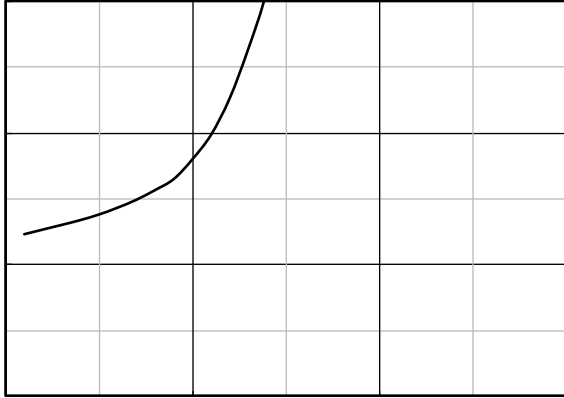


Figure 2. Transfer Characteristics



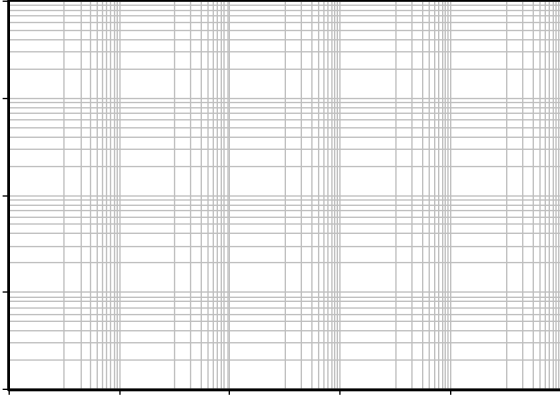


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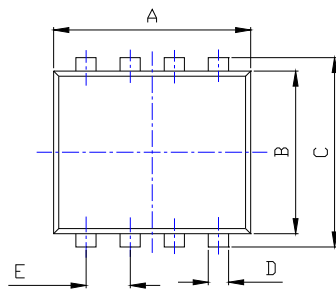


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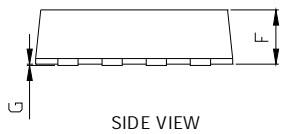




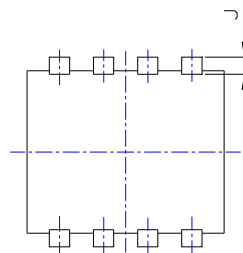
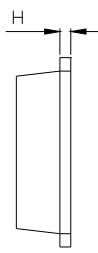
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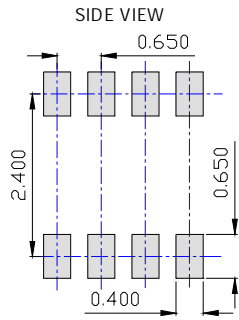
TOP VIEW



SIDE VIEW



BOTTOM VIEW



UNIT mm

SUGGESTED SOLDER PAD LAYOUT

DIMENSIONS				
SYMBOL	INCHES		Millimeter	
	MIN.	MAX.	MIN.	MAX.
A	0.108	0.120	2.750	3.050
B	0.089	0.100	2.250	2.550
C	0.104	0.116	2.650	2.950
D	0.008	0.016	0.200	0.400
E	0.026TYP		0.650TYP	
F	0.028	0.035	0.700	0.900
G	0.000	0.004	0.000	0.100
H	0.004	0.012	0.100	0.300
J	0.007	0.015	0.190	0.390

NOTE:

- 1.PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
- 2.TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.
- 3.THE PAD LAYOUT IS FOR REFERENCE PURPOSES ONLY.



D-

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